RF ports are arranged on two sides of the board, the A side (PA0 – PA4) and the B side (PB0 – PB4). The "zero" ports (PA0 and PB0) are always each connected to one other port but never to each other. When PA0 is connected to an A side port, PB0 must be connected to a B side port. When PA0 is connected to a B side port, PB0 must be connected to an A side port.

Active LED at PA0 and port connected to PA0 is green.
Active LED at PB0 and port connected to PB0 is yellow.

Default I2C address is 0x30/0x31.
Use jumpers to change address up to 0x3E/0x3F.

Control logic guarantees valid RF switch configuration and reduces the number of external control signals required.

U1CTRL: Set low to swap sides (PA0 connected to P*B, PB0 connected to P*A). Set high to connect same sides (default).

U2CTRL1	U2CTRL0	selected port
low	low	PA1
low	high	PA2
high	low	PA3
high	high	PA4 (default)

U3CTRL1	U3CTRL0	selected port
low	low	PB1
low	high	PB2
high	low	PB3
high	high	PB4 (default)

Quick mode: When enabled by !OE (U4 IO7), RF switches are controlled by GPIO or SCT instead of I2C. U4 IO2 through IO6 must be configured as inputs.

optional headers for control by GreatFET instead of HackRF

test points

mounting holes are connected to GND

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